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	Application No.	Applicant(s)
Notice of Allowability	10/647,242	TAKEHIRO HASEGWAA
	Examiner	Art Unit
	Son L. Mai	2827
The MAILING DATE of this communication appearable claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) of NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RICE of the Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in the or other appropriate communication is sub and MPEP 1308.	is application. If not included cation will be mailed in due course. THIS
	<u> </u>	•
2. The allowed claim(s) is/are <u>1-30</u> .		
3. The drawings filed on <u>26 August 2003</u> are accepted by the	Examiner.	
 4. Acknowledgment is made of a claim for foreign priority und a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 	been received.	
3. Certified copies of the phonty documents have	, .	
International Bureau (PCT Rule 17.2(a)).	aments have been received in	Timo national stage application from the
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminiful INFORMAL PATENT APPLICATION (PTO-152) which give		_
 6. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftsperson 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date 	on's Patent Drawing Review (Amendment / Comment or in	the Office action of
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the ne header according to 37 CFR 1	drawings in the front (not the back) of 1.121(d).
 DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT F 		
Attachment(s) 1. Notice of References Cited (PTO-892)	5. Notice of Infor	mal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Sum	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	•	ail Date nendment/Comment
Paper No./Mail Date <u>8-26-03;1-23-04</u> 4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's St	atement of Reasons for Allowance
of Biological Material	9. Other	
		fle
		Son L. Mai
		Patent Examiner
		Art Unit 2827

DETAILED ACTION

1. The papers filed 08-26-03 and 01-23-04 have been entered. Claims 1-30 are pending in the application.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

3. The information disclosure statements filed 08-26-03 and 01-23-04 have been considered.

Allowable Subject Matter

- 4. Claims 1-30 are allowed.
- 5. The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or suggest a semiconductor memory device comprising: memory cells each of which includes a floating gate MOS transistor and a select MOS transistor in series with the floating gate MOS transistor, select gate lines each of which is formed by connecting commonly the gates of the select MOS transistors of the memory cells in the same row, and first metal wiring layers which are provided for every select gate lines, each of which is formed in the row direction so as to pass through almost the central part of the memory cells, is connected electrically to the corresponding one of the select gate lines, and transmits a row select signal for the second row decoder to select the select gate line.

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6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The references cited in form PTO-892 disclose memory cells in semiconductor memory devices wherein each memory cell comprises a floating gate transistor connected in series with a select transistor.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Son L. Mai whose telephone number is 571-272-1786. The examiner can normally be reached on 8am to 6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

04-22-05

SON L. MAI PRIMARY EXAMINER